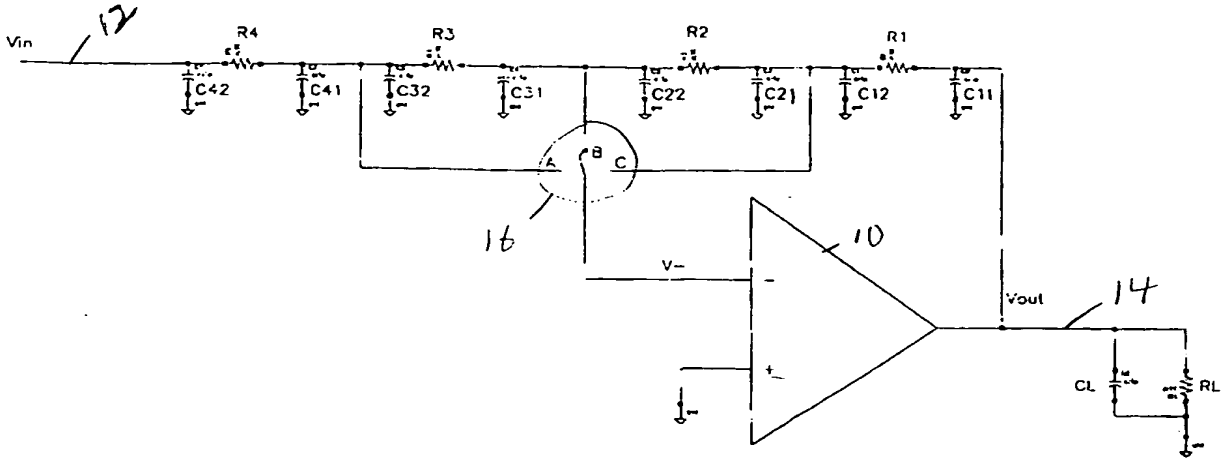

FIG. 1
PRIOR ART

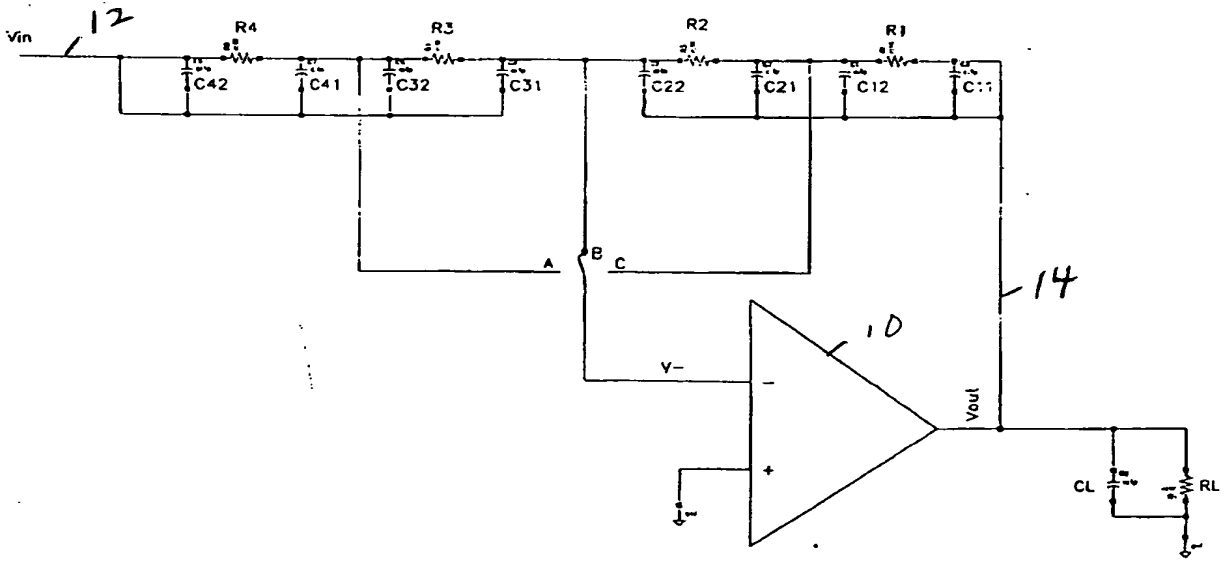

FIG. 2

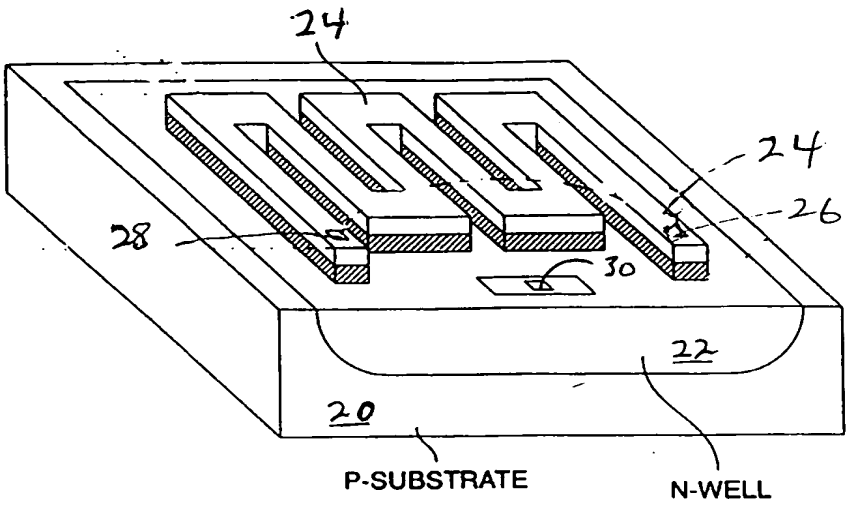

FIG. 3

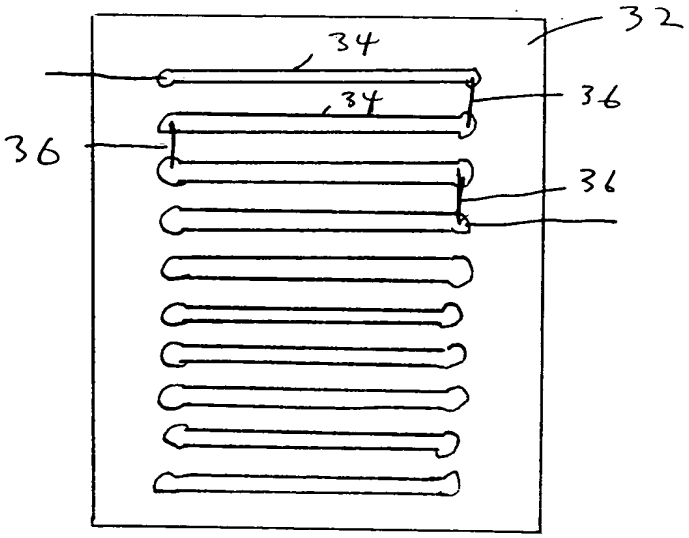

FIG. 4

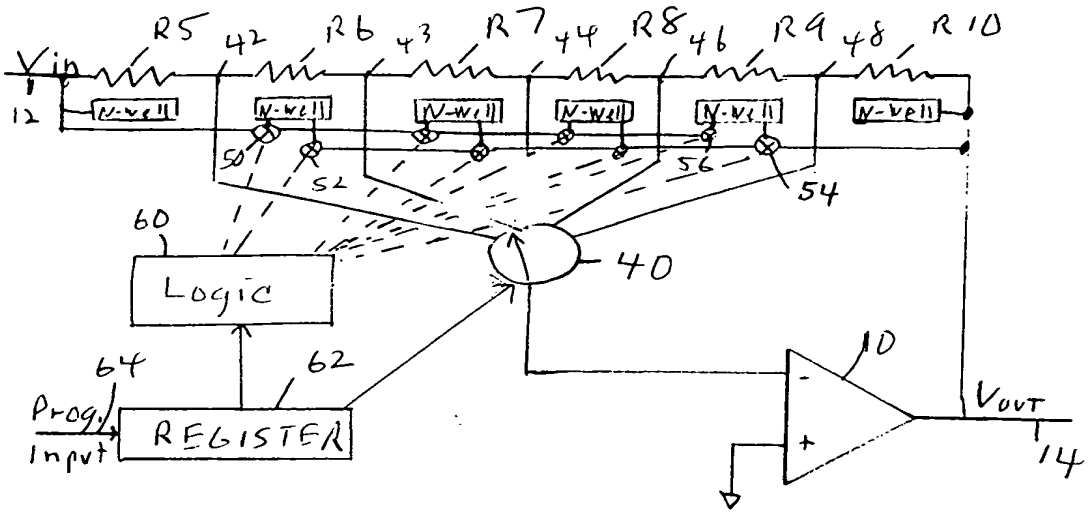
R5
42
R6
43
R7
44
R8
46
R9
48
R10
Vin
12
N-well
50
52
54
56
60
Logic
40
64
62
Prog. Input
REGISTER
10
VOUT
14

FIG. 5